

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No.	Application No.:
	ALTRP051C1	10/776,684
	Applicant:	
	Karchmer et al.	
	Filing Date	Group
	February 10, 2004	2123

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
DC	A	5,490,266	02/1996	Sturges			
DC	B	5,513,118	04/1996	Dey et al.			
DC	C	5,519,627	05/1996	Mahmood et al.			
DC	D	5,623,418	04/1997	Rostoker et al.			
DC	E	5,673,198	09/1997	Lawman et al.			
DC	F	5,848,236	12/1998	Dearth et al.			
DC	G	5,870,608	02/1999	Gregory			
DC	H	5,920,711	07/1999	Searight et al.			
DC	I	5,937,190	08/1999	Gregory et al.			
DC	J	5,991,533	11/1999	Sano et al.			
DC	K	6,044,211	03/2000	Jain			
DC	L	6,173,241	01/2001	Trimberger			
DC	M	6,212,650	04/2001	Guccione			
DC	N	6,269,467	07/2001	Chang et al.			
DC	O	6,317,860	11/2001	Heile			
DC	P	6,421,808	07/2002	McGeer et al.			

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
DC	Q	Gajski et al. "A design Methodology and Environment for Interactive Behavioral Synthesis", University of California, Irvine, 1996 pp. 1-21.
DC	R	Kock, et al. "Breakpoints and Breakpoint Detection in Source-Level Emulation, ACM, 1998, pp. 209-230
DC	S	Chaiyakul et al., "Assignment Decision Diagram for High-Level Synthesis", Dec. 12, 1992, Technical Report #92-103, Dept. of Information and Computer Science, University California, Irvine CA 92717
DC	T	Chaiyakul et al., "High-Level Transformation for Minimizing Syntactic Variances", ACM 1993, pp. 413-418
Examiner /Dwin Craig/ (10/26/2006)		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.